## WHAT IS CLAIMED IS:

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1. A semiconductor integrated circuit apparatus mounted on a predetermined circuit board, the apparatus comprising:

semiconductor information storage means for storing semiconductor information unique to the semiconductor integrated circuit apparatus, and

semiconductor information output means connected to the semiconductor information storage means for reading out the semiconductor information from the semiconductor information storage means in response to a signal supplied from outside and outputting the read-out semiconductor information to the outside.

15 2. The semiconductor integrated circuit apparatus according to claim 1,

wherein the semiconductor information output means comprises

connection control means, which is to be connected to
predetermined external storage means, for controlling a
write-inoperation of information to the external storage means,
and

control means for controlling the write-in operation to write the read-out semiconductor information into a predetermined region of the external storage means via the connection control means.

- 3. The semiconductor integrated circuit apparatus according to claim 1,
- wherein the semiconductor information output means comprises

connection control means, which is to be connected to predetermined external storage means storing a program, for controlling a read-out operation of the program stored in the external storage means, the program being used for executing the read-out operation of the semiconductor information, and

control means for controlling the read-out operation and external outputting operation of the semiconductor information based on the read-out program via the connection control means.

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4. The semiconductor integrated circuit apparatus according to claim 1,

the semiconductor information storage means stores an identification code as the semiconductor information, the identification code being assigned to allow identification of the semiconductor integrated circuit apparatus, and outputs an electric signal according to the identification code in response to an input of a read-out signal.

20 5. A circuit board on which a semiconductor integrated circuit apparatus is mounted, the circuit board comprising:

storage means for allowing reading out from and writing in to outside,

semiconductor information storage means for storing semiconductor information unique to the semiconductor information circuit apparatus, and

semiconductor information output means, which is to be connected to the semiconductor information storage means, for reading out the semiconductor information from the semiconductor information storage means in response to a signal supplied from outside and writing the read-out semiconductor

information into the storage means.

6. The circuit board according to claim 5,

wherein the storage means stores a program being used for executing the read-out operation of the semiconductor information, and

wherein the semiconductor information output means controls the read-out operation of the semiconductor information based on the program read out from the storage means, and the write-in operation of the semiconductor information to the storage means.

7. An information readout method of reading out semiconductor information of a semiconductor integrated circuit apparatus, the method comprising the steps of:

writing a program, which is for reading out semiconductor information unique to the semiconductor integrated circuit apparatus and stored in the semiconductor integrated circuit apparatus, into a predetermined external storage means,

reading the program written into the external storage means and reading out the semiconductor information based on the program, and

writing the read-out semiconductor information into a predetermined region of the external storage means.

8. A semiconductor integrated circuit apparatus mounted on a predetermined circuit board, the apparatus comprising:

a semiconductor information storage section for storing semiconductor information unique to the semiconductor integrated circuit apparatus, and

a semiconductor information output section connected

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to the semiconductor information storage section for reading out the semiconductor information from the semiconductor information storage section in response to a signal supplied from outside and outputting the read-out semiconductor information to the outside.